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Lageman, J.A.; Pence, J.A.;

Military Communications Conference, 1989. MILCOM '89. Conference Record. 'Bridging the Gap. Interoperability, Survivability, Security',, 1989 IEEE , 15-18 Oct. 1989

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2 Neuromorphic analog communication

Marienborg, J.-T.; Lande, T.S.; Hovin, M.; Abusland, A.;

Neural Networks, 1996., IEEE International Conference on , Volume: 2 , 3-6 June 1996

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[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **IEEE CNF**

3 Performance evaluation model for CSMA/CD bus under heavy load

Yan-puLiu; Shi-binxin; Huang-tiaXia;

TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on Issue: 0 , 19-21 Oct. 1993

Page(s): 487 -491 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(196 KB\)\]](#) **IEEE CNF**

4 A new architecture and pr t c l f r high perf rmance integrated I cal area netw rks

Agrawal, J.P.; Place, J.P.; Dreiling, D.R.;

Military Communications Conference, 1989. MILCOM '89. Conference Record. 'Bridging the Gap. Interoperability, Survivability, Security'., 1989 IEEE , 15-18 Oct. 1989

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5 CSMA/CD/TDMA: a dynamic combination for voice and data integration

Wei-Hong Chen; Chung-Chin Lu;

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6 A topology-aware collision resolution algorithm

Barnett, L.;

Local Computer Networks, 1994. Proceedings., 19th Conference on , 2-5 Oct. 1994

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7 Performance assessment of a deterministic access protocol for high performance bus topology LANs

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Computers and Communications, 1995. Proceedings., IEEE

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8 CSMA/CD protocol for time-constrained communication on bus networks

Jan, R.-H.; Yeh, Y.-J.;

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Bolis, S.; Economou, E.G.; Philokyprou, P.G.;
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139 Issue: 4 , Aug. 1992
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10 Unslotted 1- and p_i-persistent CSMA-CD protocols for fiber optic bus networks

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11 The role of medium access control protocols in SCADA systems

Luque, J.; Gomez, I.;
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12 On the nature and inadequacies of transport timing delay constructs in VHDL descriptions

Walker, P.A.; Ghosh, S.;
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13 A high performance LAN/MAN using a distributed dual mode control protocol

Yuang, M.C.; Chen, M.-C.;
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14 Sample size reduction in Monte Carlo based use-of-system costing power systems

Wijayatunga, P.D.C.; Cory, B.J.;

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15 Multi-access protocol for voice/data integration on a twin-bus local area network

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3 Introduction of a new laboratory course: Computer Aided Electronic Circuit Design-Simulation

Kuraishi, G.;

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4 A signalling system for multiprocessors

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6 Control of advanced static VAr generator by using recurrent neural networks

Wei Chen; Yongqiang Liu; Jun Chen; Jie Wu;

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7 Induction motor modelling and interfacing technique for fast transient stability simulation

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8 A novel control scheme for the actively clamped resonant DC-link inverter

Dehua Zhang; Jianping Ying; Qian Liu; Teng Liu; Fanbin Wang;

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Yan Deng; Xiangning He;

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11 Contributions of individual generators to complex power losses and flows. II. Algorithm and simulations

Peng, J.-C.; Jiang, H.;

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Fujiwara, M.; Yokokohji, Y.; Yoshikawa, T.;
Haptic Interfaces for Virtual Environment and Teleoperator Systems, 2003. HAPTICS 2003. Proceedings. 11th Symposium on , 22 and 23 Mar 2003

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2 Narrow bandwidth polarisation scrambler using delay-coupled binary phase pulse for super-dense WDM networks

Fujiwara, M.; Teshima, M.; Iwatsuki, K.;
Electronics Letters , Volume: 39 Issue: 5 , 6 March 2003
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3 12.5-GHz-spaced super-dense WDM ring network handling 256 wavelengths with tapped-type OADMs

Takachio, N.; Suzuki, H.; Fujiwara, M.; Kani, J.; Teshima, M.; Iwatsuki, K.; Kitoh, T.;
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4 Passivation property of SiN_x/H/SiO₂/double layer formed by ammonia microwave remote plasma cvd method

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5 Device performance of sub-50 nm CMOS with ultra-thin plasma nitrided gate dielectrics

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6 Run-to-run control of multidimensional crystal size distribution in a batch crystallizer

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7 Mechanism configuration evaluation of a linear-actuated parallel mechanism using manipulability

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8 1000-channel SD-WDM light source employing optical multicarrier generation scheme using sinusoidal amplitude-phase hybrid modulation

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9 High performance 35 nm gate length CMOS with NO oxynitride gate dielectric and Ni salicide

Inaba, S.; Okano, K.; Matsuda, S.; Fujiwara, M.; Hokazono, A.; Adachi, K.; Ohuchi, K.; Suto, H.; Fukui, H.; Shimizu, T.; Mori, S.; Oguma, H.; Murakoshi, A.; Itani, T.; Iinuma, T.; Kudo, T.; Shibata, H.; Taniguchi, S.; Takayanagi, M.; Azuma, A.; Oyamatsu, Electron Devices, IEEE Transactions on , Volume: 49 Issue: 12 , Dec. 2002
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10 12.5 GHz spaced 1.28 Tb/s (512-channel×2.5 Gb/s) super-dense WDM transmission over 320 km SMF using multiwavelength generation technique

Suzuki, H.; Fujiwara, M.; Takachio, N.; Iwatsuki, K.; Kitoh, T.; Shibata, T.;

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11 Optical carrier supply module applicable to over 100 super-dense WDM systems of 1000 channels

Teshima, M.; Fujiwara, M.; Kani, J.-I.; Suzuki, H.; Amemiya, M.; Takachio, N.; Iwatsuki, K.;

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12 A study of analog characteristics of CMOS with heavily nitrided NO oxynitrides

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13 A high performance 100 nm generation SOC technology (CMOS IV) for high density embedded memory and mixed signal LSIs

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14 High performance 35 nm gate length CMOS with NO oxynitride gate dielectric and ni SALICIDE

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15 Development and application of optical topography for human brain functions - new measurement methodology for infant brain functions

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16 Flattened optical multicarrier generation of 12.5 GHz spaced 256 channels based on sinusoidal amplitude and phase hybrid modulation

Fujiwara, M.; Kani, J.; Suzuki, H.; Araya, K.; Teshima, M.;
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17 DAQ system for LEPS experiment

Sugaya, Y.; Ahn, J.K.; Akimune, H.; Asano, Y.; Chang, W.C.; Date, S.; Fujiwara, M.; Hicks, K.; Hotta, T.; Imai, K.; Ishikawa, T.; Iwata, T.; Kawai, H.; Kim, Z.Y.; Kishimoto, Y.; Kumagai, N.; Makino, S.; Matsukoka, N.; Matsumura, T.; Mibe, T.; Minami, S.;
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19 Extending gate dielectric scaling limit by NO oxynitride: design and process issues for sub-100 nm technology

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24 Optimized halo structure for 80 nm physical gate CMOS technology with indium and antimony highly angled ion implantation

Miyashita, K.; Yoshimura, H.; Takayanagi, M.; Fujiwara, M.; Adachi, K.; Nakayama, T.; Toyoshima, Y.;
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26 New optimization guidelines for sub-0.1 μ m CMOS technologies with 2 nm NO gate oxynitrides

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27 Optical video/audio signal distribution network for a broadcast center using WD/TD/SD hybrid multiplexing

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